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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/410,202	09/30/1999	BRIAN DONOVAN	7134.007	6524

7590

08/28/2002

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EXAMINER

ENG, DAVID Y

ART UNIT

PAPER NUMBER

2155

DATE MAILED: 08/28/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

09/410,202

Applicant(s)

DONOVAN, BRIAN

Examiner

DAVID Y. ENG

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 28 May 2002.
- 2a) ☒ This action is **FINAL**.                      2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-16 is/are pending in the application.
- 4a) Of the above claim(s) 1 and 3 is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 2 and 4-16 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on \_\_\_\_\_ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

**Priority under 35 U.S.C. §§ 119 and 120**

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

**Attachment(s)**

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) \_\_\_\_\_.
- 4) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_.

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Applicants are suggested to cancel the nonelected claims 1 and 3. Claims 10-16 have been added. Claims 1-16 are pending. Claims 1 and 3 are withdrawn from consideration. The active claims are 2 and 4-16.

Claim 8 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In claim 8, there is no functional relationship and connection between the trace enable circuit and the rest of the components recited in its parent claim. The trace enable circuit as recited has nothing to do with task execution in accordance with priority.

The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.

Claims 2, 4, 7 and 14-16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Madnick.

Claim 8 is rejected under 35 U.S.C. 103(a) as being unpatentable over Madnick in view of George.

Claims 9 and 10-13 are rejected under 35 U.S.C. 103(a) as being unpatentable over Madnick in view of Jen.

Details of the rejection have already been set forth in the last Office action. The details are incorporated herein by reference thereto.

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In the communication filed on May 28, 2002, Applicants stated that Madnick is a text book written in 1974, that the procedures in Madnick are carried out manually and not by circuits, that the circuits for determining task priority are not available at the time the Madnick text book was published (1974) and that microcomputer had not been invented yet, and whereas the instant invention is a circuit formed by logic elements for determining task priority to be executed by a processor.

As point out in the last Office action, Madnick taught the invention as claimed. Madnick is a computer text book describing how a computer which is made of logic circuits processes multiple tasks including task scheduling. As indicated by Magar (4,507,727) in column 1, US patent 3,757,306 discloses a microprocessor made up of integrated circuit chips filed on August 1971. Claim 1 (line 15) of Rubin (4,628,158) having an issue date earlier than the instant filing date calling for a job scheduler having logic elements for scheduling jobs to be executed by processor. It can be seen that Madnick's task scheduler is also implemented by IC chips which are made up of logic components which are made up of transistors or gates.

**THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after

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the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

A handwritten signature in black ink, appearing to read 'D. Eng', with a long horizontal stroke extending to the right.

DAVID Y. ENG  
PRIMARY EXAMINER